



Test Report : SFTN01L-12N

1W SMD Package DC-DC Unregulated Converter

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

■ SAFETY TEST

Safety Test

■ RELIABILITY TEST

Other Test

DESIGN VERIFY TEST
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDITC
1	VOLTAGE ACCURACY	-5.0% ~ +5.0 %	I/P:5VDC O/P:FULL LOAD Ta:25°C	-0.95%	P
2	RIPPLE & NOISE	100 mVp-p	I/P:5VDC O/P:FULL LOAD Ta:25°C	30 mV	P
3	LINE REGULATION	-12% ~ +12%	I/P:4.5VDC~5.5VDC O/P:FULL LOAD Ta:25°C	-11.64% ~ +11.38%	P
4	LOAD REGULATION	-8% ~ +8%	I/P:5VDC O/P:10% LOAD~FULL LOAD Ta:25°C	-3.22% ~ +3.53%	P

INPUT FUNCTION TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDITC
1	EFFICIENCY	78% (Typ)	I/P:5VDC O/P:FULL LOAD Ta:25°C	78.97%	P
2	DC CURRENT	257 mA / FULL LOAD 28 mA / NO LOAD	I/P:5VDC O/P:NO / FULL LOAD Ta:25°C	252.1 mA / FULL LOAD 32.5 mA / NO LOAD	P

PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDITC
1	SHORT PROTECTION	0.5 second max.	I/P:5.5VDC O/P:FULL LOAD Ta:25°C	OK	P

SAFETY TEST
SAFETY TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDITC
1	WITHSTAND VOLTAGE	I/P-O/P:3K VDC/min	I/P-O/P:3K VDC/min Ta:25°C	I/P-O/P: 0.002mA NO DAMAGE	P
2	ISOLATION RESISTANCE	I/P-O/P:500VDC>10GΩ	I/P-O/P:500 VDC Ta:25°C	I/P-O/P>10GΩ NO DAMAGE	P

■ RELIABILITY TEST**OTHER**

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDITC
1	MTBF	MIL-HDBK-217F,GB,25°C TOTAL FAILURE RATE : 0.956937 M.T.B.F : 1045 KHRS			P

TEST RESULT	TESTER	APPROVAL
PASS	ARCHEN HSIAO	PETER CHENG