



# Test Report: ELG-200-48

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200W Constant Voltage + Constant Current LED Driver

## ■ DESIGN VERIFY TEST

- Output Function Test
- Input Function Test
- Protection Function Test
- Component Stress Test

## ■ SAFETY & E.M.C. TEST

- Safety Test
- E.M.C. Test

## ■ RELIABILITY TEST

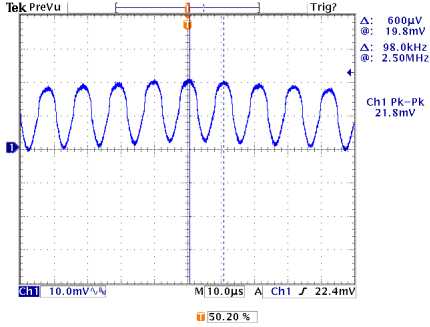
- Environment Test

## DESIGN VERIFY TEST

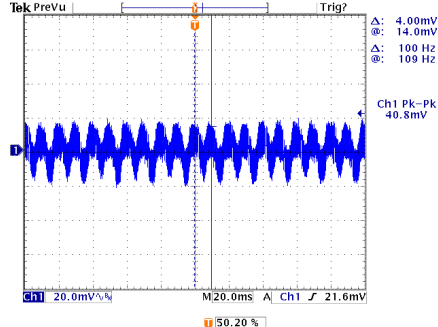
### OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	CONSTANT CURRENT REGION	24V~48V	I/P: 230VAC O/P: LED MODE Ta: 25°C	17V~ 48V
2	OUTPUT VOLTAGE ADJUST RANGE (For A-Type only)	44.8V~51.2V	I/P: 230VAC O/P: NO LOAD Ta: 25°C	42.97V~ 52.98V
3	OUTPUT CURRENT ADJUST RANGE (For A-Type only)	2.08A~4.16A	I/P: 230VAC O/P: SETTING Ta: 25°C	2.05A~ 4.19A
4	OUTPUT VOLTAGE TOLERANCE	-2%~+2%	I/P: 100VAC / 305VAC O/P: 95% / NO LOAD Ta: 25°C	-0.27%~ 0.35%
5	LINE REGULATION	-0.5%~+0.5%	I/P: 200VAC ~ 305VAC O/P: 95% LOAD Ta: 25°C	0%~ 0%
6	LOAD REGULATION	-0.5%~+0.5%	I/P: 230VAC O/P: 95% ~NO LOAD Ta: 25°C	-0.04%~ 0.04%
7	OVER/UNDERSHOOT TEST	<± 5 %	I/P: 230VAC O/P: 95% LOAD Ta: 25°C	<5 %
8	RIPPLE & NOISE (Max)	250mVp-p	I/P: 230VAC O/P: 95% LOAD Ta: 25°C	40.8 mVp-p

high frequency :



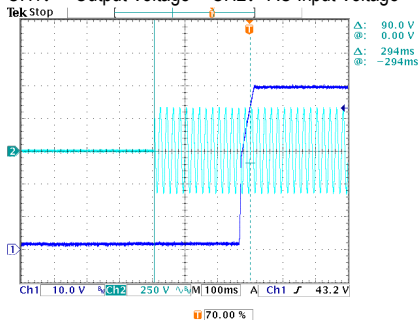
low frequency :



9	SET UP TIME(Max)	230VAC/ 500ms 115VAC/ 1000ms	I/P: 230 VAC I/P: 115 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	230VAC/ 294 ms 115VAC/ 362 ms
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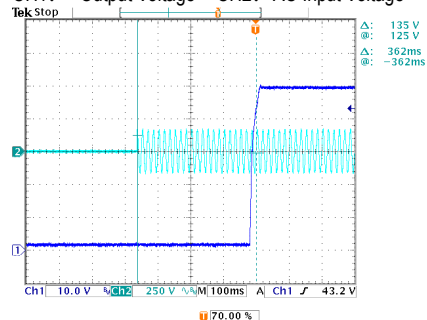
INPUT=230VAC/50HZ @ 95% LOAD

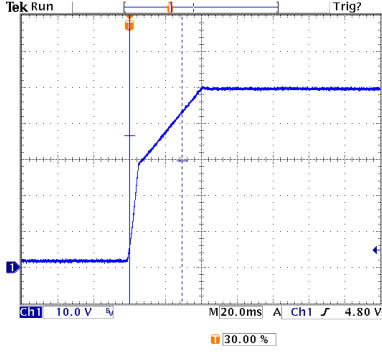
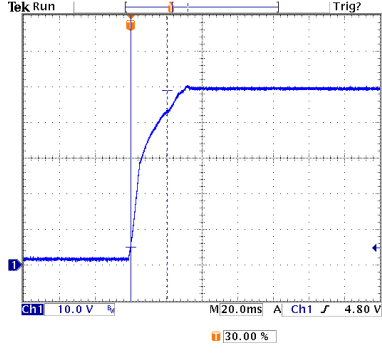
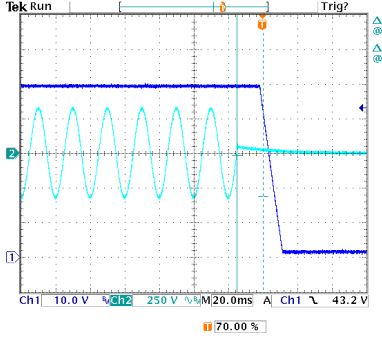
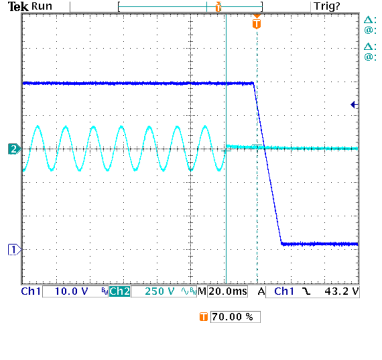
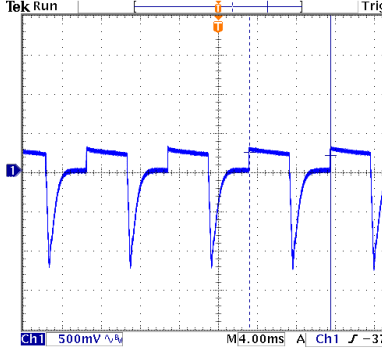
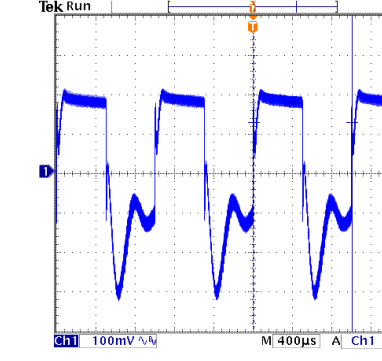
CH1: Output Voltage CH2: AC Input Voltage



INPUT=115VAC/60HZ @ 75% LOAD

CH1: Output Voltage CH2: AC Input Voltage

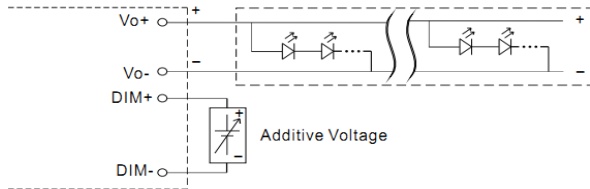


10	RISE TIME (Max)	230VAC/ 100ms 115VAC/ 100ms	I/P: 230 VAC I/P: 115 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	230VAC/ 29.2 ms 115VAC/ 20.4 ms
<p>INPUT=230VAC/50HZ @ 95% LOAD</p> <p>CH1: Output Voltage</p> 		<p>INPUT=115VAC/60HZ @ 75% LOAD</p> <p>CH1: Output Voltage</p> 		
11	HOLD UP TIME(Typ)	230VAC/ 10ms 115VAC/ 10ms	I/P: 230 VAC I/P: 115 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	230VAC/ 15.2 ms 115VAC/ 18.4 ms
<p>INPUT=230VAC/50HZ @ 95% LOAD</p> <p>CH1: Output Voltage CH2: AC Input Voltage</p> 		<p>INPUT=115VAC/60HZ @ 75% LOAD</p> <p>CH1: Output Voltage CH2: AC Input Voltage</p> 		
12	DYNAMIC LOAD	V1: 4800 mVp-p	I/P: 230VAC O/P: (1)95%/50% LOAD 50%DUTY / 120HZ (2)95%/50% LOAD 50%DUTY / 1KHZ Ta: 25°C	(1) 1560 mVp-p (2) 524 mVp-p
<p>FULL /50% LOAD 50%DUTY / 120HZ</p> 		<p>FULL /50% LOAD 50%DUTY / 1KHZ</p> 		

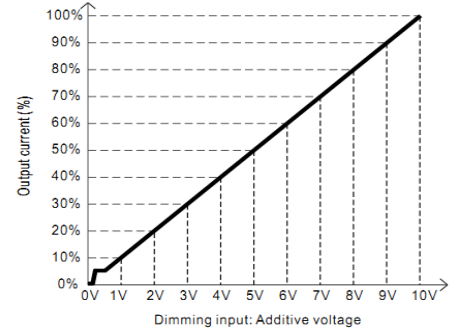
13 DIMMING OPERATION (for B-Type)

- Output constant current level can be adjusted by applying one of the three methodologies between DIM+ and DIM-: 0 ~ 10Vdc, or 10V PWM signal or resistance.
- Direct connecting to LEDs is suggested. It is not suitable to be used with additional drivers.
- Dimming source current from power supply: 100uA(typ.)

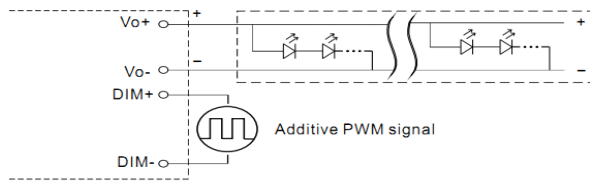
© Applying additive 0 ~ 10VDC



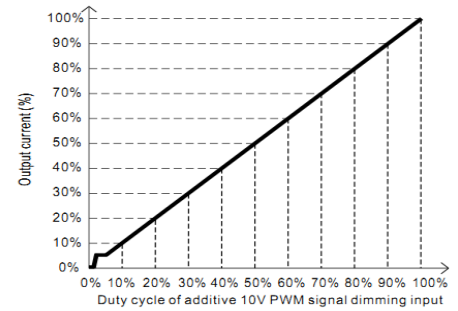
"DO NOT connect "DIM- to Vo-"



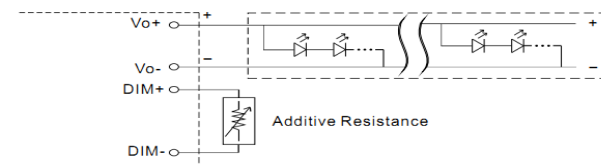
© Applying additive 10V PWM signal (frequency range 100Hz ~ 3KHz):



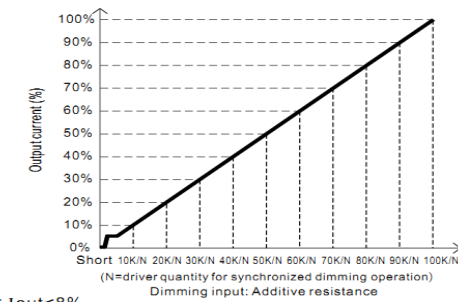
"DO NOT connect "DIM- to Vo-"



© Applying additive resistance:



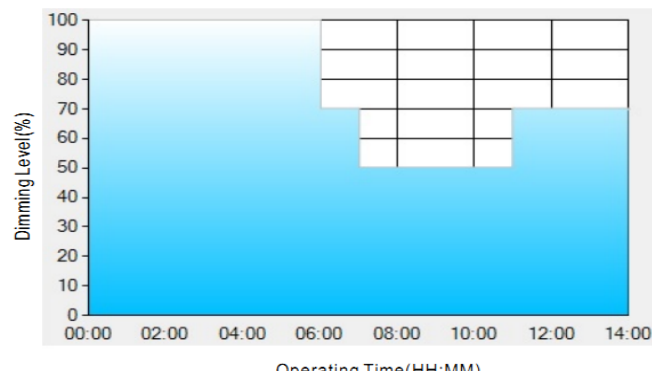
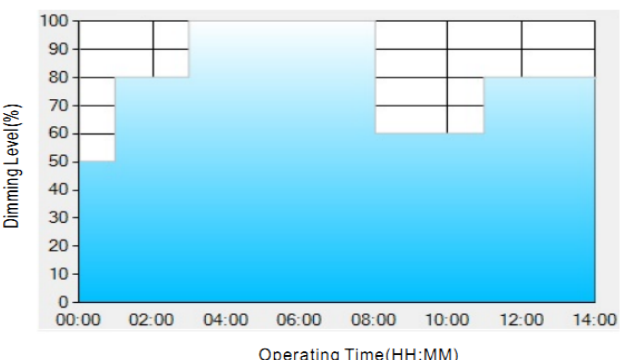
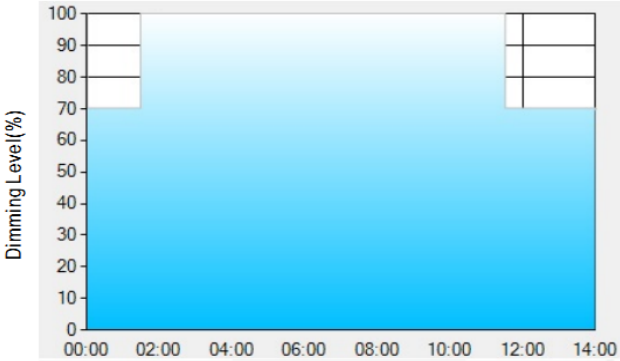
"DO NOT connect "DIM- to Vo-"



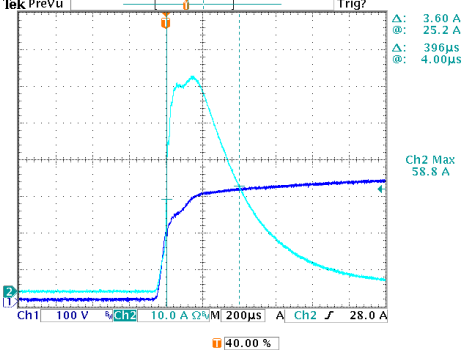
Note : 1. Min. dimming level is about 8% and the output current is not defined when 0% < Iout < 8%.  
 2. The output current could drop down to 0% when dimming input is about 0kΩ or 0Vdc, or 10V PWM signal with 0% duty cycle.

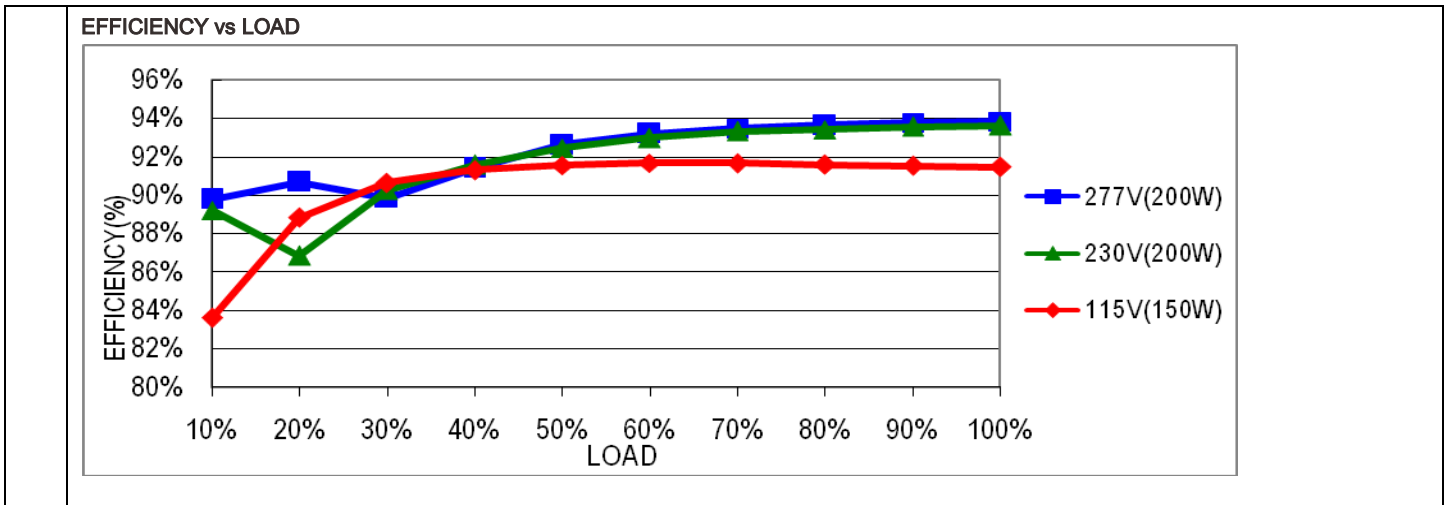
	V	Short	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	OPEN
1	Output Current	0	0.371	0.789	1.208	1.627	2.045	2.464	2.882	3.300	3.719	4.136	4.160
	%	0%	8.92%	18.97%	29.04%	39.11%	49.16%	59.23%	69.28%	79.33%	89.40%	99.42%	100.00%
2	PWM(100Hz)	0V	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	OPEN
	Output Current	0	0.448	0.862	1.260	1.676	2.074	2.487	2.887	3.302	3.700	4.067	4.160
	%	0%	10.77%	20.72%	30.29%	40.29%	49.86%	59.78%	69.40%	79.38%	88.94%	97.76%	100.00%
3	R	0%	10K	20K	30K	40K	50K	60K	70K	80K	90K	100K	OPEN
	Output Current	0	0.382	0.807	1.240	1.671	2.107	2.541	2.982	3.426	3.869	4.263	4.160
	%	0%	9.18%	19.40%	29.81%	40.17%	50.65%	61.08%	71.68%	82.36%	93.00%	102.48%	100.00%

TEST RESULT: OK

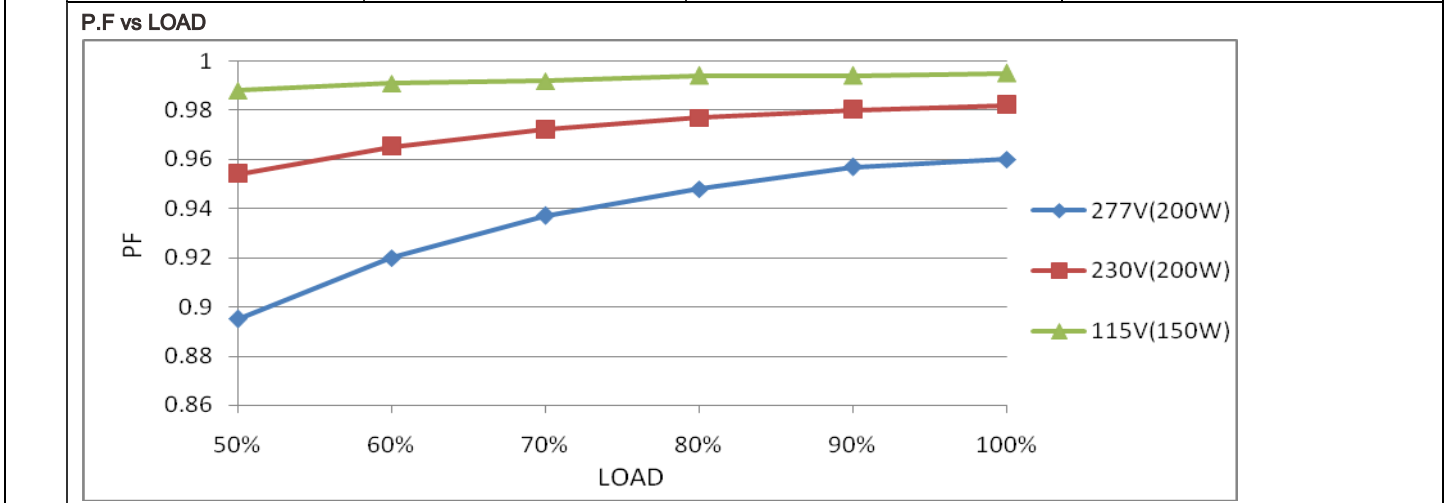
14	<p>DIMMING OPERATION (primary side, for DA-Type)</p>	<p>※DALI Interface          ·Apply DALI signal between DA+ and DA-.          ·DALI protocol comprises 16 groups and 64 addresses.          ·First step is fixed at 8% of output. Please contact MEAN WELL for other setup.</p> <p>I/P: 230 VAC          O/P: DIMMING TEST          Ta: 25°C          TEST RESULT: OK</p>																																													
15	<p>DIMMING OPERATION (for DXX-Type by User definition)</p>	<p>※Smart timer dimming function          ·MEAN WELL Smart timer dimming primarily provides the adaptive proportion dimming profile for the output constant current level to perform up to 14 consecutive hours. 3 dimming profiles hereunder are defined accounting for the most frequently seen applications. If other options may be needed, Please contact MEAN WELL for other setup.          Ex: ☉ D01-Type: the profile recommended for residential lighting</p>  <p>Set up for D01-Type in Smart timer dimming software program:</p> <table border="1" data-bbox="981 750 1508 884"> <thead> <tr> <th></th> <th>T1</th> <th>T2</th> <th>T3</th> <th>T4</th> </tr> </thead> <tbody> <tr> <td>TIME**</td> <td>06:00</td> <td>07:00</td> <td>11:00</td> <td>---</td> </tr> <tr> <td>LEVEL**</td> <td>100%</td> <td>70%</td> <td>50%</td> <td>70%</td> </tr> </tbody> </table> <p>Ex: ☉ D02-Type: the profile recommended for street lighting</p>  <p>Set up for D02-Type in Smart timer dimming software program:</p> <table border="1" data-bbox="909 1164 1524 1288"> <thead> <tr> <th></th> <th>T1</th> <th>T2</th> <th>T3</th> <th>T4</th> <th>T5</th> </tr> </thead> <tbody> <tr> <td>TIME**</td> <td>01:00</td> <td>03:00</td> <td>8:00</td> <td>11:00</td> <td>---</td> </tr> <tr> <td>LEVEL**</td> <td>50%</td> <td>80%</td> <td>100%</td> <td>60%</td> <td>80%</td> </tr> </tbody> </table> <p>Ex: ☉ D03-Type: the profile recommended for tunnel lighting</p>  <p>Set up for D03-Type in Smart timer dimming software program:</p> <table border="1" data-bbox="997 1590 1428 1736"> <thead> <tr> <th></th> <th>T1</th> <th>T2</th> <th>T3</th> </tr> </thead> <tbody> <tr> <td>TIME**</td> <td>01:30</td> <td>11:00</td> <td>---</td> </tr> <tr> <td>LEVEL**</td> <td>70%</td> <td>100%</td> <td>70%</td> </tr> </tbody> </table> <p>I/P: 230 VAC          O/P: DIMMING TEST          Ta: 25°C          TEST RESULT: OK</p>		T1	T2	T3	T4	TIME**	06:00	07:00	11:00	---	LEVEL**	100%	70%	50%	70%		T1	T2	T3	T4	T5	TIME**	01:00	03:00	8:00	11:00	---	LEVEL**	50%	80%	100%	60%	80%		T1	T2	T3	TIME**	01:30	11:00	---	LEVEL**	70%	100%	70%
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LEVEL**	70%	100%	70%																																												

## INPUT FUNCTION TEST

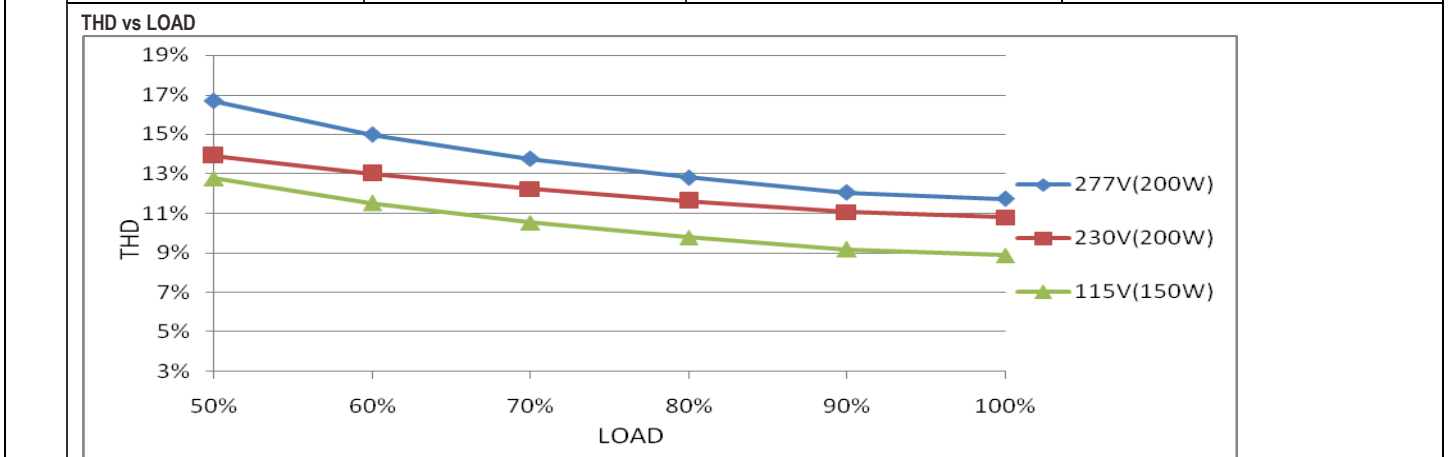
NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	100VAC~305VAC	I/P: TESTING O/P: 95% LOAD Ta: 25°C	97 V~ 305 V
			I/P: LOW-LINE-3V=97 V HIGH-LINE+10V=315 V O/P: 95%/NO LOAD ON: 30 Sec OFF: 30 Sec 10MIN ( POWER ON/OFF NO DAMAGE )	TEST: OK
2	INPUT FREQUENCY RANGE	47HZ ~63 HZ NO DAMAGE	I/P: 100 VAC ~305 VAC O/P: 95%-NO LOAD Ta: 25°C	TEST: OK
3	AC CURRENT	1.8A/115VAC 1.2A/230VAC 1.0A/277VAC	I/P: 115 VAC I/P: 230 VAC I/P: 277 VAC O/P: 95% LOAD/75% LOAD Ta: 25°C	I = 1.77 A/115VAC I = 0.89 A/ 230VAC I = 0.75 A/ 277VAC
4	LEAKAGE CURRENT	< 0.75mA / 277VAC	I/P: 277 VAC O/P: NO LOAD Ta: 25°C	L-FG: 0.381 mA N-FG: 0.364 mA
5	NO LOAD/STANDBY POWER CONSUMPTION	< 0.5W	I/P: 230VAC O/P: NO LOAD Ta: 25°C	0.346 W/ 230VAC
6	INRUSH CURRENT(Typ)	230V/ 60A Twidth =510us measured at 50% Ipeak COLD START	I/P: 230 VAC O/P: 95% LOAD Ta: 25°C	I = 58.8 A/ 230VAC Twidth =396 us
<p>INPUT=230VAC/50HZ @ 95% LOAD</p> <p>CH2: Input current CH1: AC Input Voltage</p>  <p>Ch1 100 V Ch2 10.0 A 50% M 200 μs A Ch2 28.0 A</p> <p>40.00 %</p>				
7	EFFICIENCY(Typ)	93%	I/P: 230VAC O/P: 95% LOAD Ta: 25°C	93.58 %



8	POWER FACTOR	0.97/ 115VAC	I/P: 115 VAC	PF=0.996 / 115VAC
		0.95/ 230VAC	I/P: 230 VAC	PF=0.986 / 230VAC
		0.92/ 277VAC	I/P: 277 VAC	PF=0.970 / 277VAC
			O/P: 95% LOAD/75% LOAD	
			Ta: 25°C	



9	TOTAL HARMONIC DISTORTION	THD < 20%	I/P: 115 VAC/50% LOAD	THD=12.08% @50% load /115VAC
		(@load ≥ 50%/115VAC, 230VAC;	I/P: 230 VAC/50% LOAD	THD=13.28% @50% load /230VAC
		@load ≥ 75%/277VAC)	I/P: 277 VAC/75% LOAD	THD=12.42% @75% load /277VAC
			Ta: 25°C	



**PROTECTION FUNCTION TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER CURRENT PROTECTION	95%~108%	I/P: 200VAC I/P: 230VAC I/P: 305VAC O/P: TESTING Ta: 25°C	100.81 %/ 200VAC 100.81 %/ 230VAC 100.81 %/ 305VAC Constant Current Limiting, recovers automatically after fault condition is removed
2	OVER VOLTAGE PROTECTION	54V~63V	I/P: 100VAC I/P: 230VAC I/P: 305VAC O/P: NO LOAD Ta: 25°C	58.83 V/ 100VAC 58.83 V/ 230VAC 58.83 V/ 305VAC Shut down o/p voltage, re-power on to recovery
3	OVER TEMPERATURE PROTECTION	NO DAMAGE	I/P: 200VAC I/P: 230VAC I/P: 305VAC O/P: 95% LOAD	O.T.P. Active Shut down o/p voltage, re-power on to recovery
4	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P: 200VAC I/P: 305VAC O/P: 95% LOAD Ta: 25°C	NO DAMAGE Hiccup mode, recovers automatically after fault condition is removed

**COMPONENT STRESS TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Power Transistor	Q 2 Rated 600V/11A	I/P: High-Line +3V =308V O/P: (1) 95% LOAD Turn on (2) Output Short (3) 95% LOAD continue Ta: 25°C	(1) 537 V (2) 504 V (3) 450 V
2	O/P Diode (MOSFET)	Q101 Rated 150V/30A	I/P: High-Line +3V =308V O/P: (1) 95% LOAD Turn on (2) Output Short (3) 95% LOAD continue Ta: 25°C	(1) 110 V (2) 16.2 V (3) 110 V
3	Input Capacitor	C5 Rated 100u/ 450V	I/P: High-Line +3V =308 V O/P: (1) 95% LOAD input on/off (2) NO LOAD input on /Off (3) 95% LOAD /NO LOAD Change Ta: 25°C	(1) 448 V (2) 448 V (3) 440 V
4	Control IC	U3 Rated 20V (MAX.)	I/P: High-Line +3V =308 V O/P: ((1) 95% LOAD (2) Output Short (3) O.L.P (4) O.V.P (5) Low Line No Load Vo(min) Ta: 25°C	(1) 16.1 V (2) 16.0 V (3) 17.3 V (4) 16.1 V (5) 12.8 V
5	PFC Power Transistor	Q 3 Rated 600V/20A	I/P: High-Line +3V =308V O/P: (1) 95% LOAD Turn on (2) Output Short (3) 95% LOAD continue Ta: 25°C	(1) 536 V (2) 418 V (3) 490 V



**SAFETY TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P-O/P: 3.75KVAC/min I/P-FG: 2.0KVAC/min O/P-FG: 1.5KVAC/min	I/P-O/P: 4.2KVAC/min I/P-FG: 2.4 KVAC/min O/P-FG: 1.8 KVAC/min Ta: 25°C	I/P-O/P: 1.907 mA I/P-FG: 3.126 mA O/P-FG: 1.478 mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P: 500VDC>100MΩ I/P-FG: 500VDC>100MΩ O/P-FG: 500VDC>100MΩ	I/P-O/P: 500 VDC I/P-FG: 500 VDC O/P-FG: 500 VDC Ta: 25°C	I/P-O/P: >9999 MΩ I/P-FG: >9999 MΩ O/P-FG: >9999 MΩ

**E.M.C TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	HARMONIC	EN61000-3-2 CLASS C	I/P: 230VAC/50HZ O/P: 95%/50% LOAD Ta: 25°C	PASS
2	CONDUCTION	EN55015	I/P: 230 VAC (50HZ) O/P: 95% LOAD Ta: 25°C	PASS Test by certified Lab
3	RADIATION	EN55015	I/P: 230 VAC (50HZ) O/P: 95% LOAD Ta: 25°C	PASS Test by certified Lab
4	E.S.D	EN61000-4-2 LIGHT INDUSTRY AIR: 8KV Contact: 4KV	I/P: 230 VAC/50HZ O/P: 95% LOAD Ta: 25°C	CRITERIA A
5	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT: 1KV	I/P: 230VAC/50HZ O/P: 95% LOAD Ta: 25°C	CRITERIA A
6	SURGE	EN61000-4-5 INDUSTRY L-N: 4KV L,N-PE: 6KV	I/P: 230VAC/50HZ O/P: 95% LOAD L-N: 4KV L,N-PE: 6KV Ta: 25°C	CRITERIA A
7	Test by certified Lab & Test Report Prepare			

## RELIABILITY TEST

### ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																																
1	TEMPERATURE RISE TEST	MODEL: ELG-200-48 1. ROOM AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: 95% LOAD Ta=34.9 °C 2. HIGH AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: 95% LOAD Ta=63.5 °C																																																																																		
				<table border="1"> <thead> <tr> <th>NO</th> <th>Position</th> <th>ROOM AMBIENT Ta=34.9 °C</th> <th>HIGH AMBIENT Ta=63.5 °C</th> </tr> </thead> <tbody> <tr><td>1</td><td>BD1</td><td>62.7°C</td><td>90.3°C</td></tr> <tr><td>2</td><td>C7</td><td>62.0°C</td><td>89.9°C</td></tr> <tr><td>3</td><td>RTH1</td><td>68.3°C</td><td>90.6°C</td></tr> <tr><td>4</td><td>Q3</td><td>64.0°C</td><td>92.0°C</td></tr> <tr><td>5</td><td>C5</td><td>59.6°C</td><td>87.0°C</td></tr> <tr><td>6</td><td>Q1</td><td>62.6°C</td><td>91.2°C</td></tr> <tr><td>7</td><td>Q2</td><td>62.6°C</td><td>91.2°C</td></tr> <tr><td>8</td><td>C16</td><td>61.2°C</td><td>89.1°C</td></tr> <tr><td>9</td><td>U3</td><td>60.4°C</td><td>87.6°C</td></tr> <tr><td>10</td><td>T1</td><td>63.1°C</td><td>90.9°C</td></tr> <tr><td>11</td><td>Q101</td><td>63.7°C</td><td>91.3°C</td></tr> <tr><td>12</td><td>Q102</td><td>62.8°C</td><td>90.7°C</td></tr> <tr><td>13</td><td>C102</td><td>59.8°C</td><td>87.3°C</td></tr> <tr><td>14</td><td>C103</td><td>58.6°C</td><td>86.1°C</td></tr> <tr><td>15</td><td>C205</td><td>59.8°C</td><td>87.3°C</td></tr> <tr><td>16</td><td>U100</td><td>55.9°C</td><td>83.2°C</td></tr> <tr><td>17</td><td>Q100</td><td>55.3°C</td><td>82.8°C</td></tr> <tr><td>18</td><td>RTH3</td><td>59.1°C</td><td>86.3°C</td></tr> <tr><td>19</td><td>TC</td><td>53.7°C</td><td>80.8°C</td></tr> </tbody> </table>	NO	Position	ROOM AMBIENT Ta=34.9 °C	HIGH AMBIENT Ta=63.5 °C	1	BD1	62.7°C	90.3°C	2	C7	62.0°C	89.9°C	3	RTH1	68.3°C	90.6°C	4	Q3	64.0°C	92.0°C	5	C5	59.6°C	87.0°C	6	Q1	62.6°C	91.2°C	7	Q2	62.6°C	91.2°C	8	C16	61.2°C	89.1°C	9	U3	60.4°C	87.6°C	10	T1	63.1°C	90.9°C	11	Q101	63.7°C	91.3°C	12	Q102	62.8°C	90.7°C	13	C102	59.8°C	87.3°C	14	C103	58.6°C	86.1°C	15	C205	59.8°C	87.3°C	16	U100	55.9°C	83.2°C	17	Q100	55.3°C	82.8°C	18	RTH3	59.1°C	86.3°C	19	TC	53.7°C	80.8°C
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2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P: 305VAC/100VAC O/P: 95% LOAD/75% LOAD Ta= -45°C /-15°C	TEST: OK																																																																																
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 60°C NO DAMAGE	I/P: 305VAC O/P: 95% LOAD Ta=60°C HUMIDITY= 95 %R.H	TEST: OK																																																																																
4	TEMPERATURE COEFFICIENT	±0.03 %/°C (0~50°C)	I/P: 230 VAC O/P: 95% LOAD	±0.002 %/°C (0~50°C)																																																																																
5	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature: -45°C~+90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 5 CYCLE 5. Input/Output condition: STATIC		TEST: OK																																																																																



6	THERMAL SHOCK TEST	1. Thermal shock Temperature: -45°C~+65°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 10 CYCLE 5. Input/Output condition: 230VAC/95% LOAD AC ON/OFF TEST AC on 3 sec/AC off 1 sec TEST	TEST: OK
7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform: Sine Wave (2) Frequency: 10~500Hz (3) Sweep Time: 12min/sweep cycle (4) Acceleration: 5G (5) Test Time: 72min in each axis (X.Y.Z) (6) Ta: 25°C	TEST: OK
8	CAPACITOR LIFE CYCLE	ELG-200-48: SUPPOSE C102 IS THE MOST CRITICAL COMPONENT (1) I/P: 230VAC O/P: FULL LOAD Tc= 70 °C LIFE TIME (2) I/P: 230VAC O/P: 75% LOAD Tc= 70 °C LIFE TIME (3) I/P: 230VAC O/P: 50% LOAD Tc= 70 °C LIFE TIME	(1) 106924 HRS (2) 127178 HRS (3) 143302 HRS
9	MTBF	Conducted by Parts Stress Analysis Prediction 2391.4K hrs min. Telcordia SR-332 (Bellcore) ; 204.9K hrs min. MIL-HDBK-217F (25°C)	
10	Ongoing Reliability Test	I/P: 230VAC O/P: FULL LOAD TA=50°C Demonstration Mean Time Between Failure : 50,000 hours	

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	ZHANGZJ/ZHUOKB	SKY	LIUWY